



1. JOB DETAILS:	
Position Title:	FPGA IP core development Engineer
Reports To	RCI
Function	Consulting
Location	RCI- Hyderabad
Grade/Level/Role	Contractual
Indicative Salary Range	Negotiable

2. JOB PURPOSE:
Role is responsible for smooth hassle free running of the Project to meet its end objectives within timelines

5. JOB REQUIREMENTS:
Qualification BE/B.Tech in ECE or EEE or EIE.
Experience: Six (3) Years) + in a similar role experience in the specialized domain. <ul style="list-style-type: none">• Programming skills: C, VHDL, Verilog & system Verilog.• Tools & Debug Skills: Simulation tools, Xilinx, ACTEL and Altera FPGA design tools.• Ability to debug at RTL level and hardware level..

“The last day for receiving applications is 10th Feb 2019.”